

**Indian Association for the Cultivation of Science
2A & 2B, Raja S.C. Mullick Road, Kolkata – 700032,
West Bengal, India.**

Corrigendum Notice No. 1 (Date: 29.03.18)

**For NIT no.:IACS/CSS/CMCD/2017-2018/125
(Tender date: 22.03.18)**

Change in Technical Specifications:

Corrected/Changed Version of CPU-GPU nodes

iii) **CPU-GPU nodes: minimum quantity should be 4**

1.	Processor	One Intel processor with x86_64 architecture with clock speed of at least 2.1 GHz , with 16 or more instructions per core per cycle and 18 or more cores per socket.
2.	GPU accelerator	One or more NVIDIA® Tesla® P100 GPU accelerator(s) with GPU memory 16 GB at 732 GB/s per node
3.	Memory	Minimum of 3 GB per core or 64 GB, whichever is higher per node

Please note all other technical specifications stated in the original tender remain unchanged other than the one mentioned above.

For any clarifications write to rcap@iacs.res.in or ssprp@iacs.res.in